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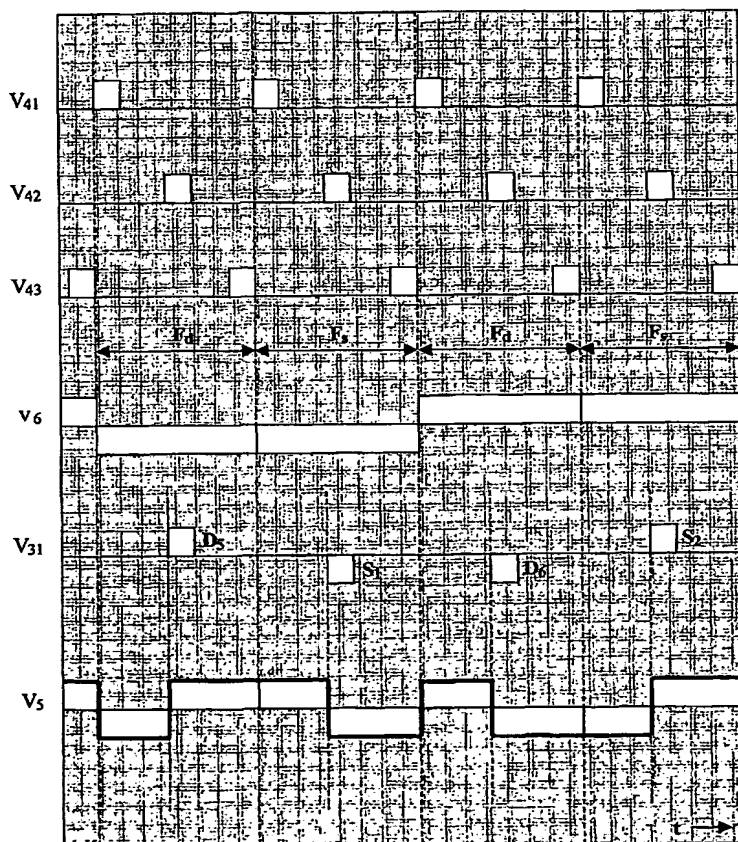
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(54) Title: ELECTROPHORETIC DISPLAY UNIT



(57) Abstract: Electrophoretic display units (1) comprising pixels (11) situated between common electrodes (6) and pixel electrodes (5) need, for shortening the total image update times, increased driving voltages across the pixels (11) which endanger transistors (12) coupled to the pixel electrodes (5). These increased driving voltage ( $V_6$ ) to the common electrode (6). To protect the transistors (12) against these increased driving voltages, a setting signal ( $S_1$ ,  $S_2$ ) is supplied to the pixel electrode (5) via the transistor (12) for reducing a voltage across the pixel (11) resulting from a transition in the alternating voltage signal ( $V_6$ ). During driving frame periods ( $F_d$ ) data pulses ( $D_1$ ,  $D_2$ ,  $D_3$ ,  $D_4$ ,  $D_5$ ,  $D_6$ ) are supplied, and during setting frame periods ( $F_s$ ), the setting signals ( $S_1$ ,  $S_2$ ) are supplied.



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